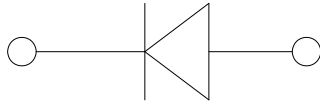
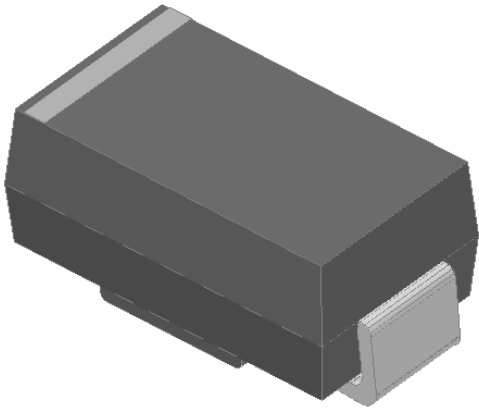




GifZUWY Acibh ; YbYfU` D ifdcgY FYWhjZjYf



:YUhi fYg

- Low profile package
- Ideal for automated placement
- Glass passivated chip junction
- High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

HmdjWU` 5dd`jWUhjcbg

For use in general purpose rectification of power supplies, inverters, converters, and freewheeling diodes for consumer, and telecommunication.

AYW\UbjWU` 8UhU

- DUW_U[Y: DO-214AC (SMA)
- Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant, halogen-free
- HYf a]bU'g: Tin plated leads, solderable per J-STD-002 and JESD22-B102
- Dc`Uf]hm: Cathode line denotes the cathode end

AUI]a i a FUh]b]g (Ta=25 Unless otherwise specified)

D5F5A9H9F	GMA6C@	IB-H	; G&55	; G&65	; G&85	; G& ; 5	; G&>5	; G&?5	; G&A5
Device marking code			GS2AA	GS2BA	GS2DA	GS2GA	GS2JA	GS2KA	GS2MA
Maximum Repetitive peak reverse voltage	V_{RRM}	V	50	100	200	400	600	800	1000
Maximum RMS Voltage	V_{RMS}	V	35	70	140	280	420	560	700
Maximum DC Blocking Voltage	V_{DC}	V	50	100	200	400	600	800	1000
Average rectified output current @60Hz sine wave, Resistance load, TL (FIG.1)	I_O	A	2						
Forward Surge Current (Non-repetitive) @60Hz Half-sine wave, 1 cycle, Tj=25	I_{FSM}	A	50						
Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, Tj=25			100						
Current squared time @1ms t 8.3ms Tj=25	I^2t	A ² s	10.735						
Storage temperature	T_{stg}		-55 ~ +150						
Junction temperature	T_j		-55 ~ +150						

9`YWhfjWU` 7\UfUWhYf]gh]Wg (Ta=25 Unless otherwise specified)

D5F5A9H9F	GMA6C@	IB-H	H9GH 7CB8-H-CBG	; G&55	; G&65	; G&85	; G& ; 5	; G&>5	; G&?5	; G&A5
Maximum instantaneous forward voltage	V_F	V	$I_{FM}=2.0A$	1.1						
Maximum DC reverse current at rated DC blocking voltage	I_R	μA	$T_j=25$	5						
			$T_j=125$	100						
Typical junction capacitance	C_j	pF	Measured at 1MHz and Applied Reverse Voltage of 4.0 V.D.C	12						

; G&55 H<F I ; G&A5'

